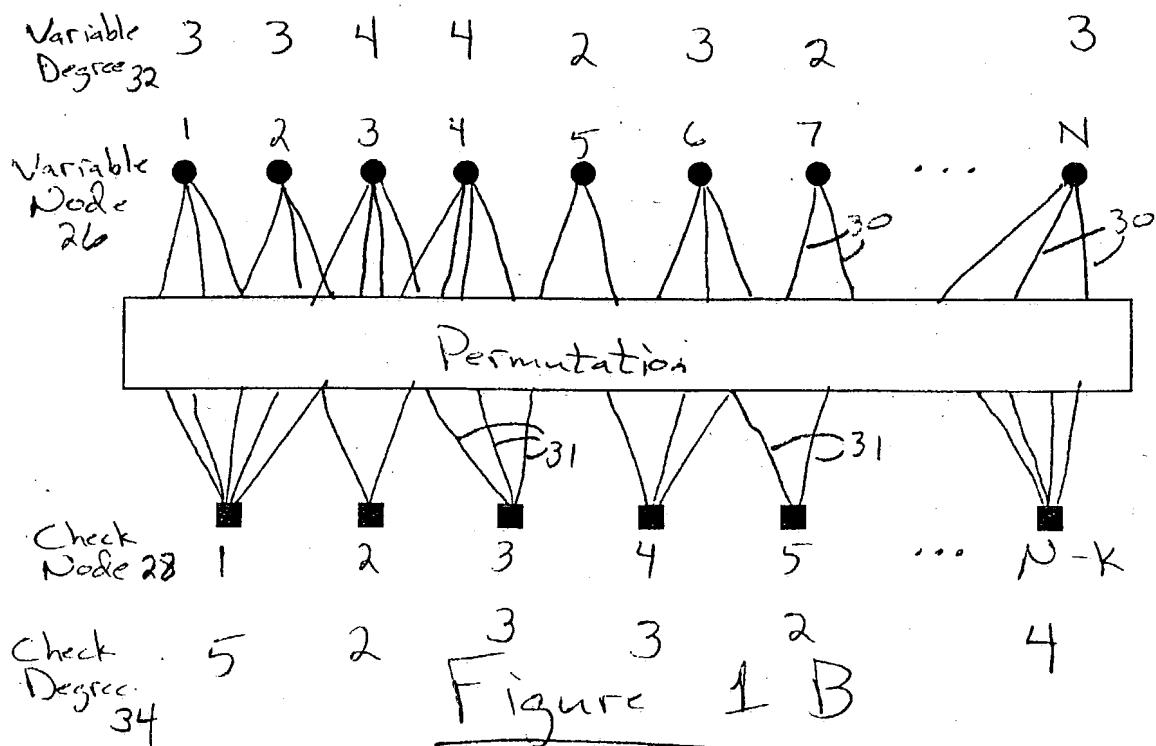


Figure 1A
PRIOR ART



PRIOR ART
Figure 2A

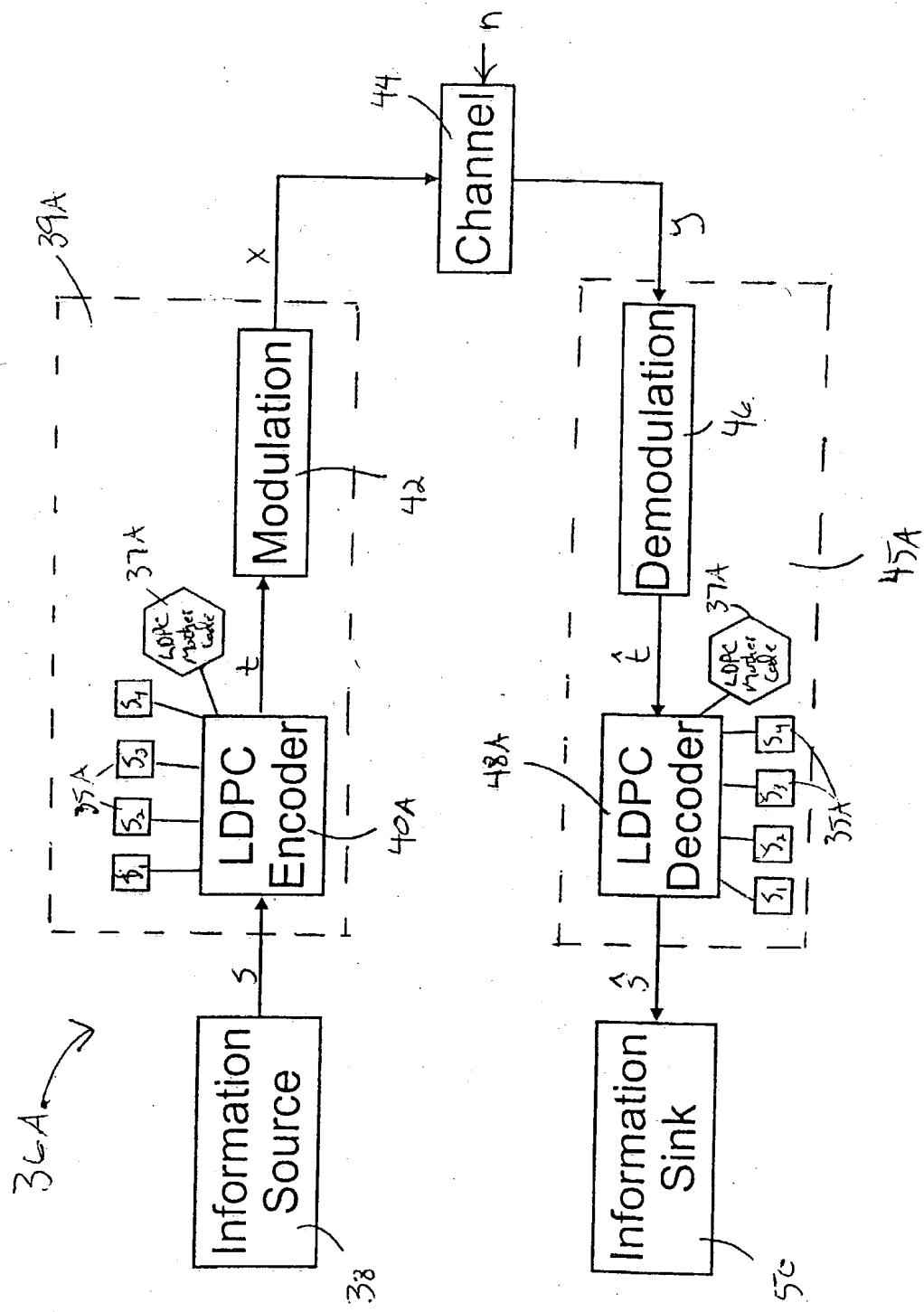


Figure 2B

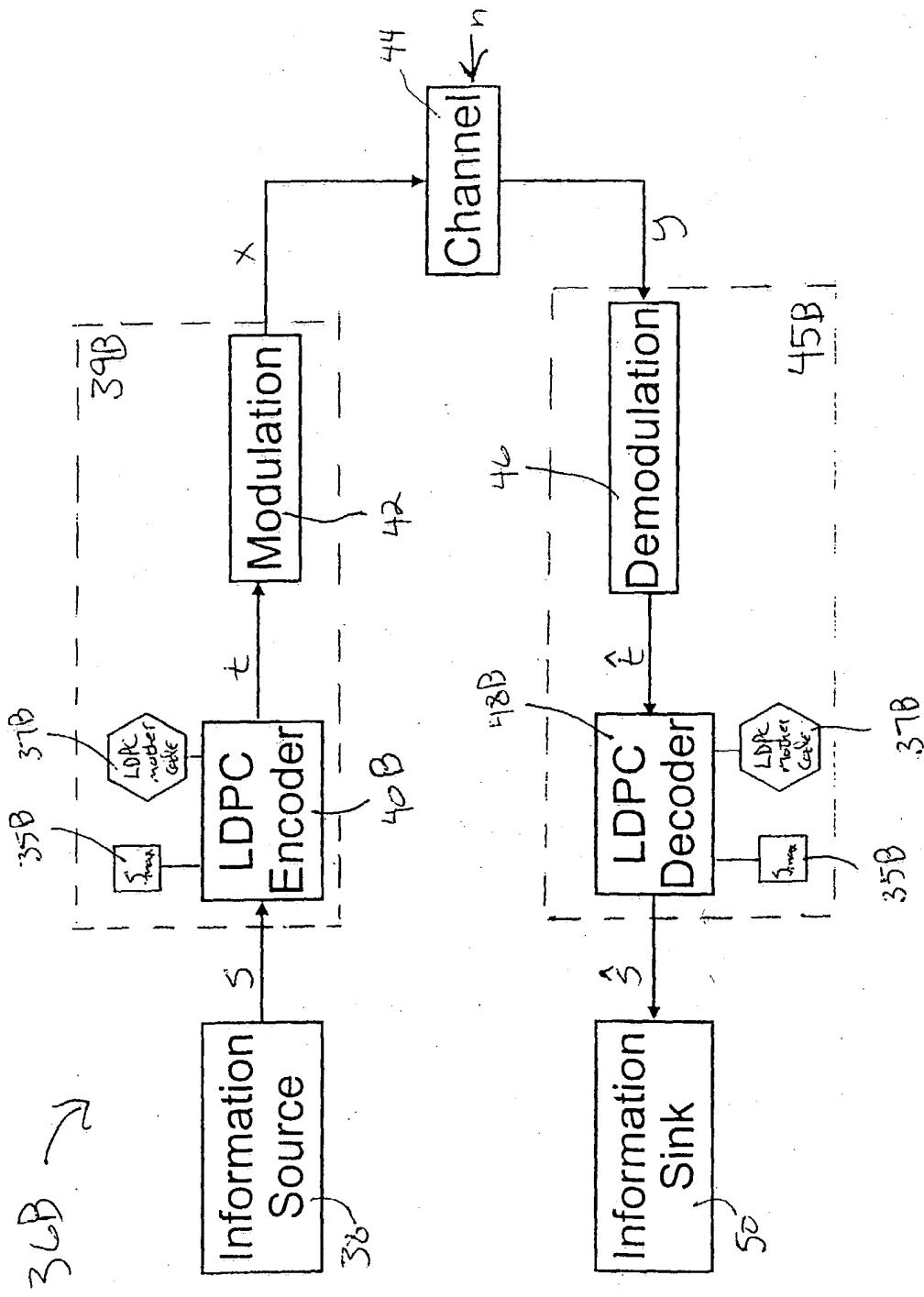
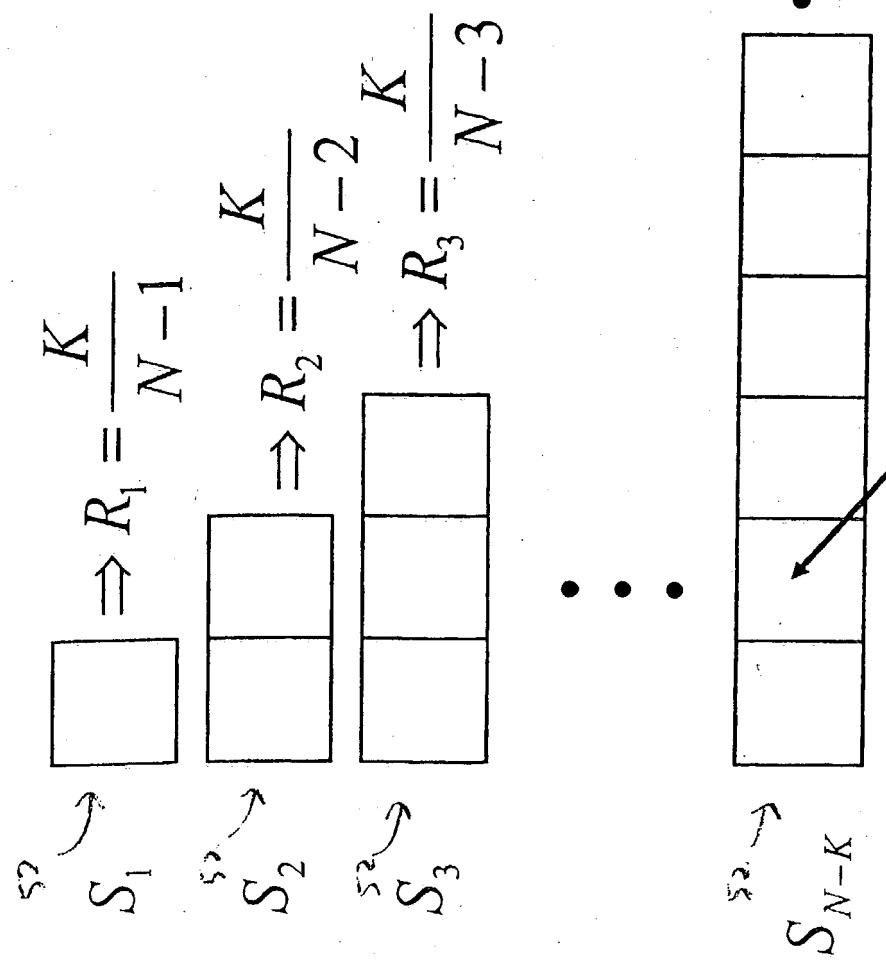


Figure 3
PRIOR ART



Memory Elements
 Contain Degree
 or Node Sequence

Figure 4

$$S_1 \subseteq S_2 \subseteq \dots \subseteq S_{N-K-1} \subseteq S_{N-K}$$

Memory Elements
Contain Degree
or Node Sequence

54

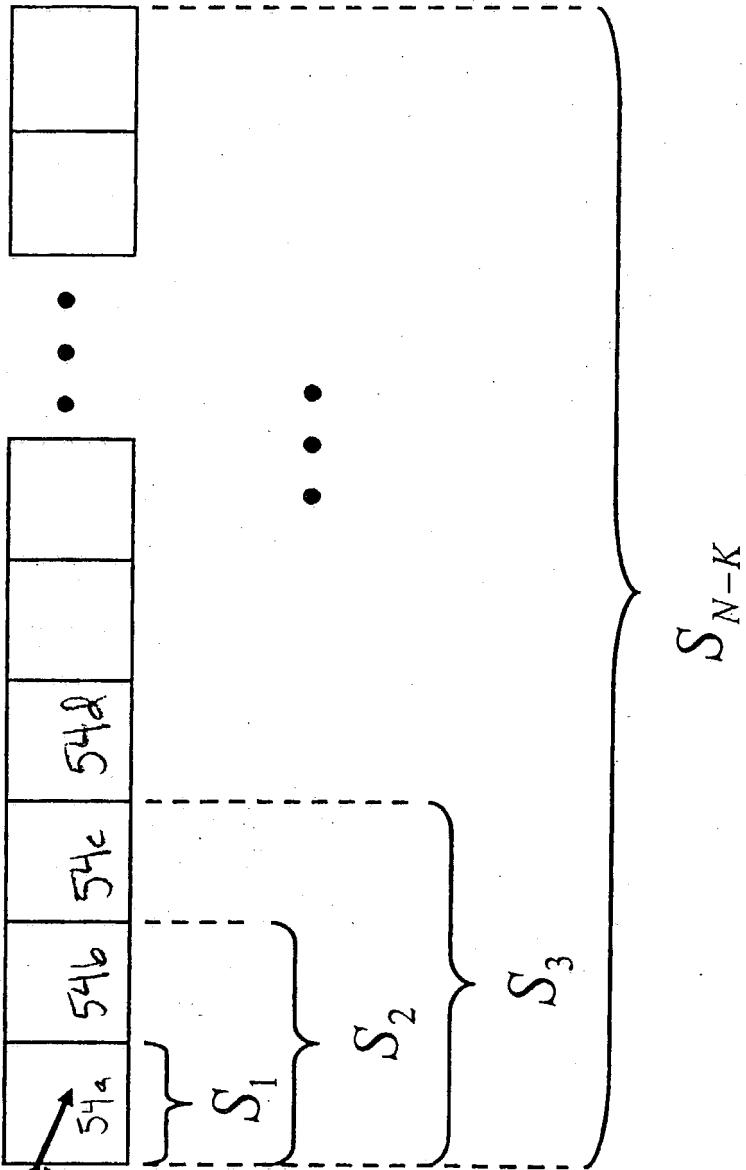


Figure 5

$$\begin{aligned}S_1 &\subseteq S_{N-K} \\S_2 &\subseteq S_{N-K} \\&\vdots\end{aligned}$$

$$S_{N-K-1} \subseteq S_{N-K}$$

Memory Elements 54
Contain Degree
or Node Sequence

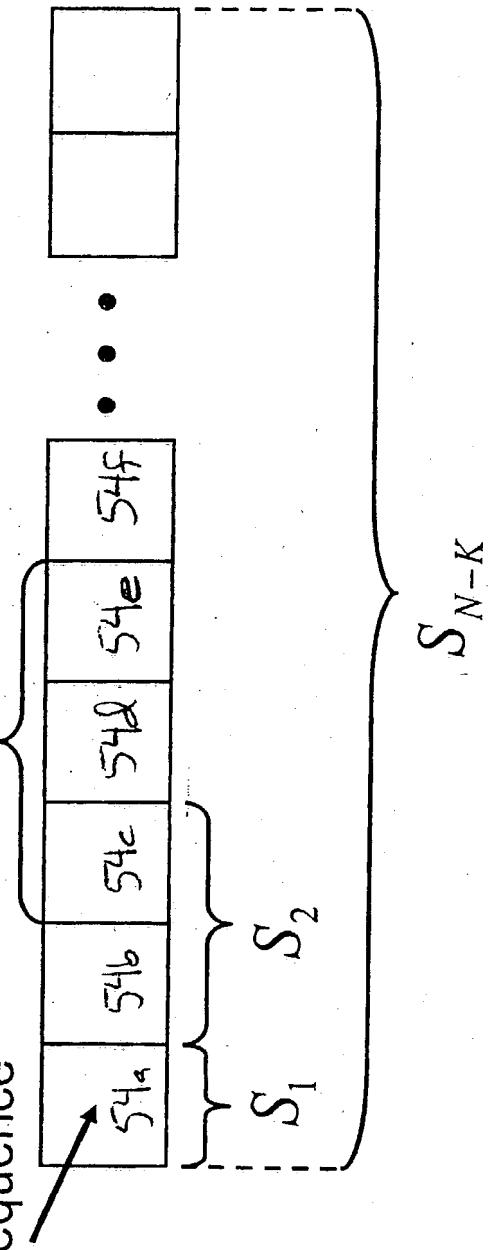


Figure 6

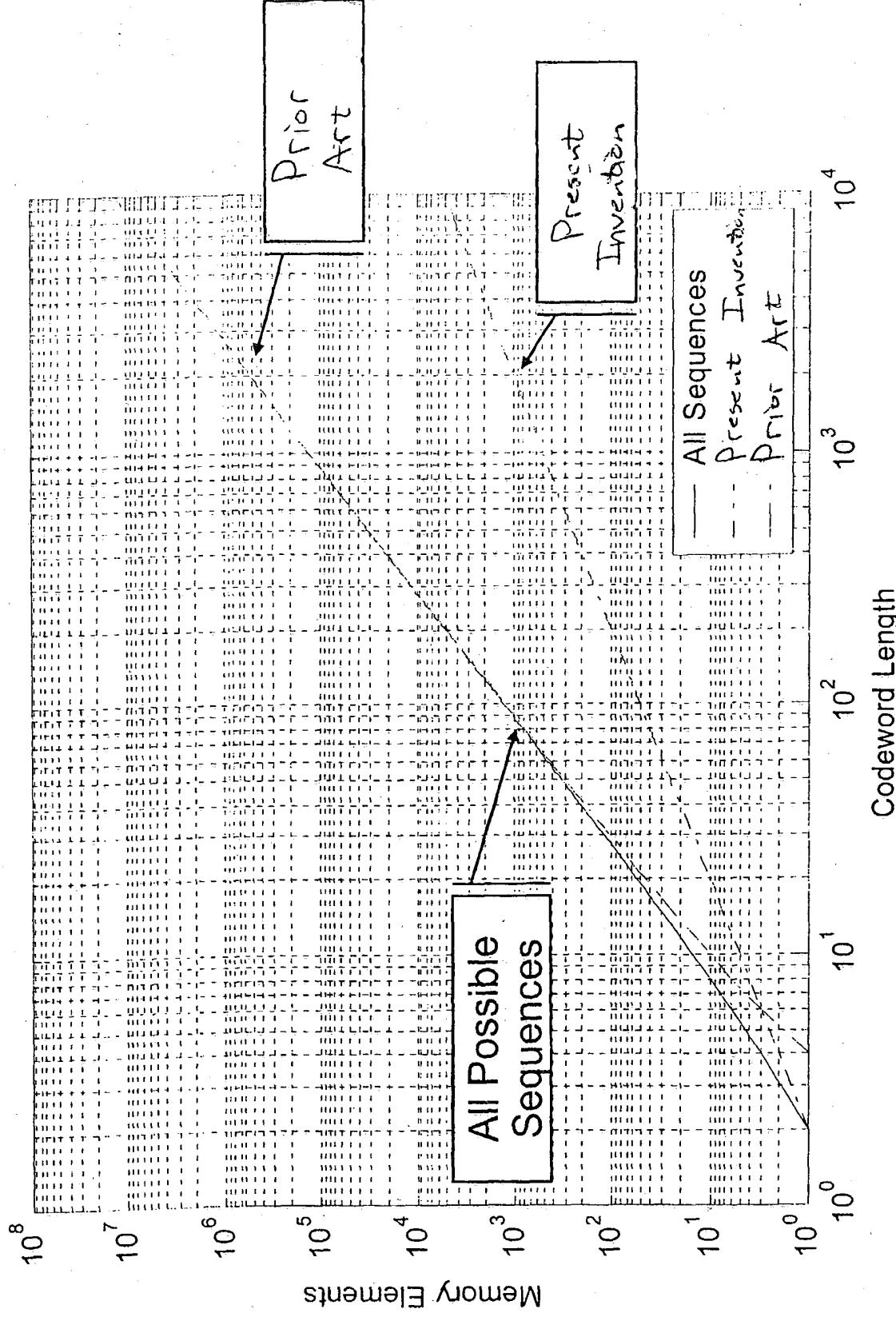


Figure 7

LDPC Punctured N1082 M541Code12.mat

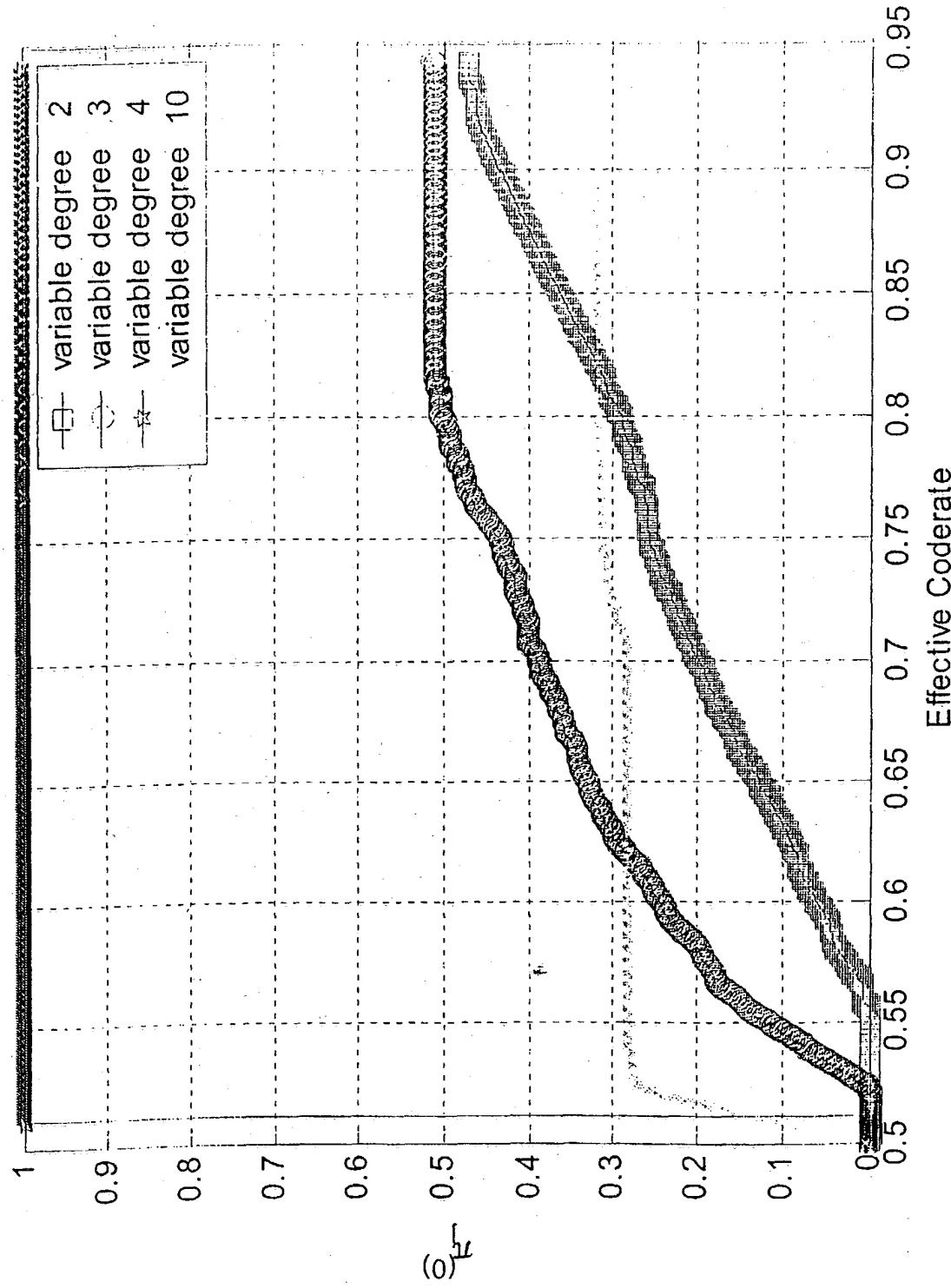


Figure 8

LDPC Punctured N3255 M1627Code11.mat

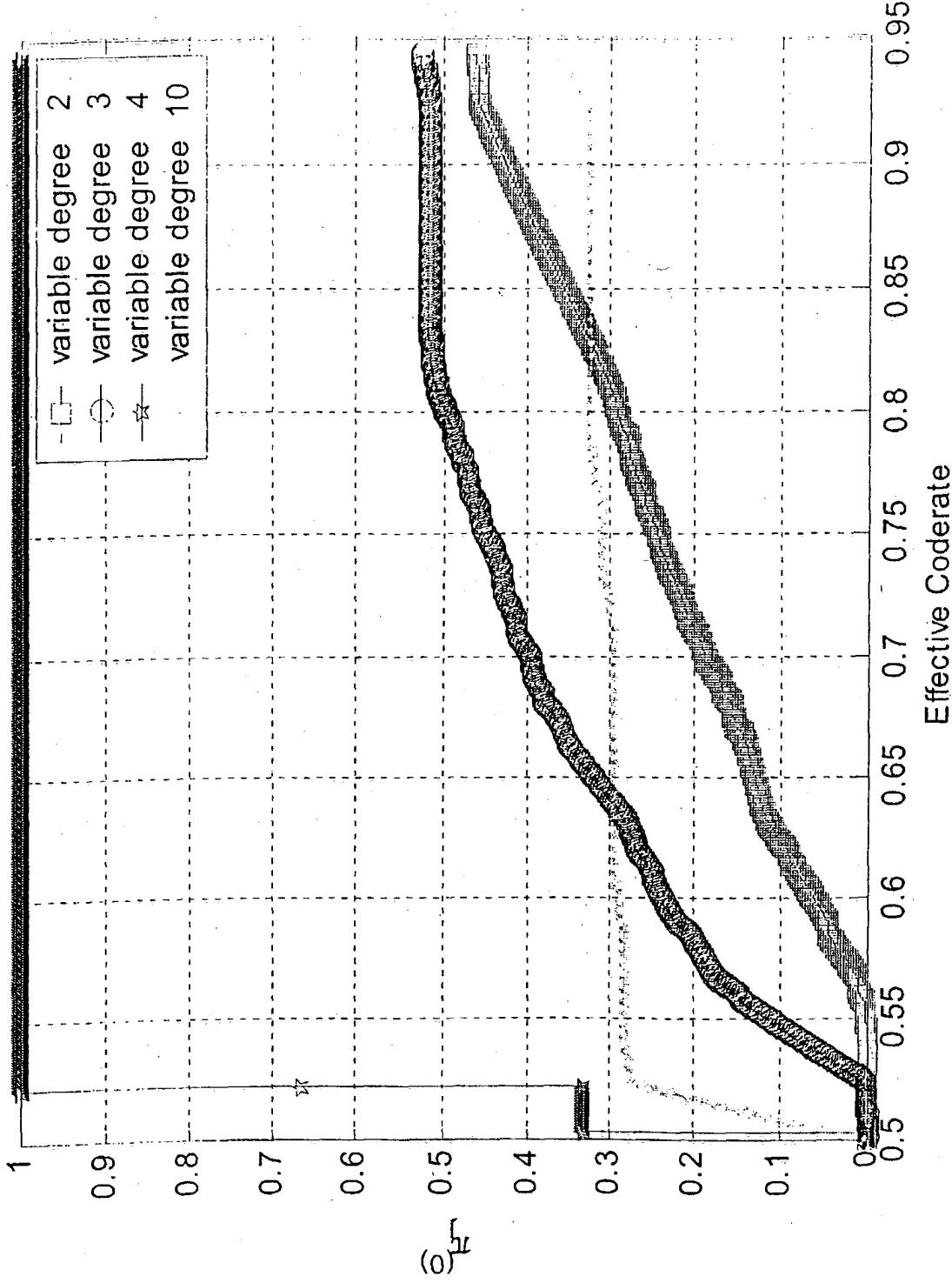


Figure 9

sim output N1082 M541Code12 100iters.mat

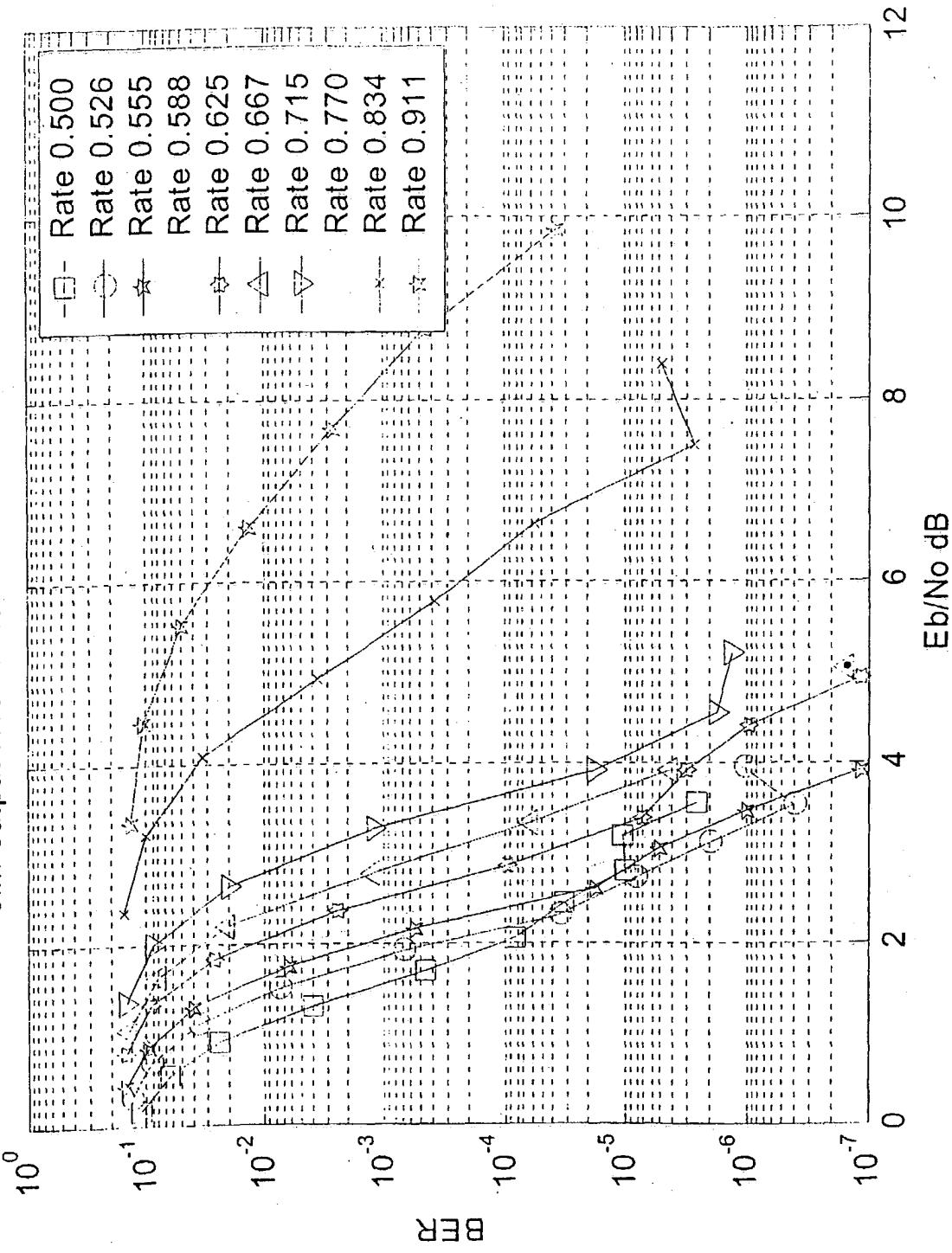


Figure 10

sim output N1082 M541Code12 100iters.mat

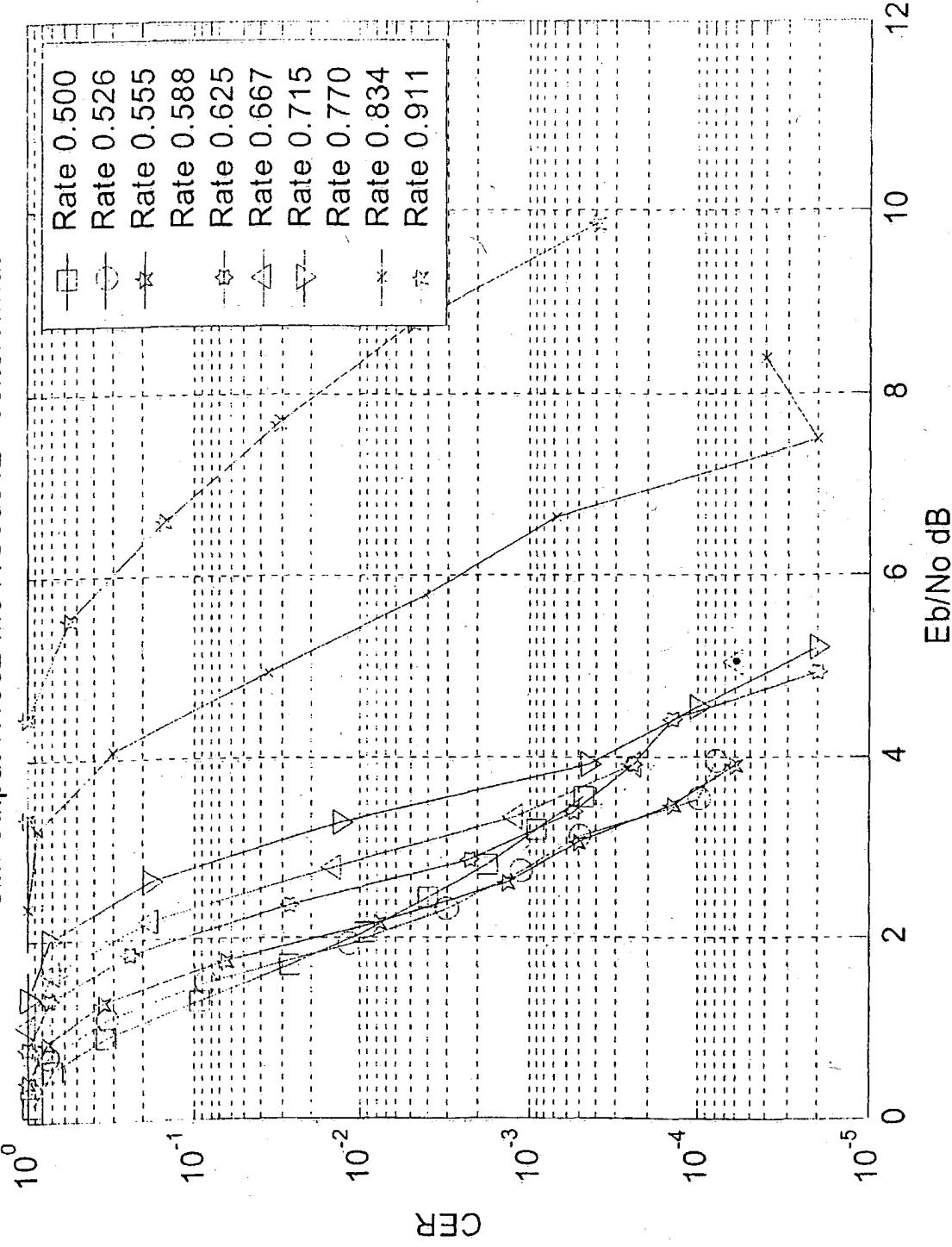


Figure 11

sim output N1082 M541Code12 100iters.mat

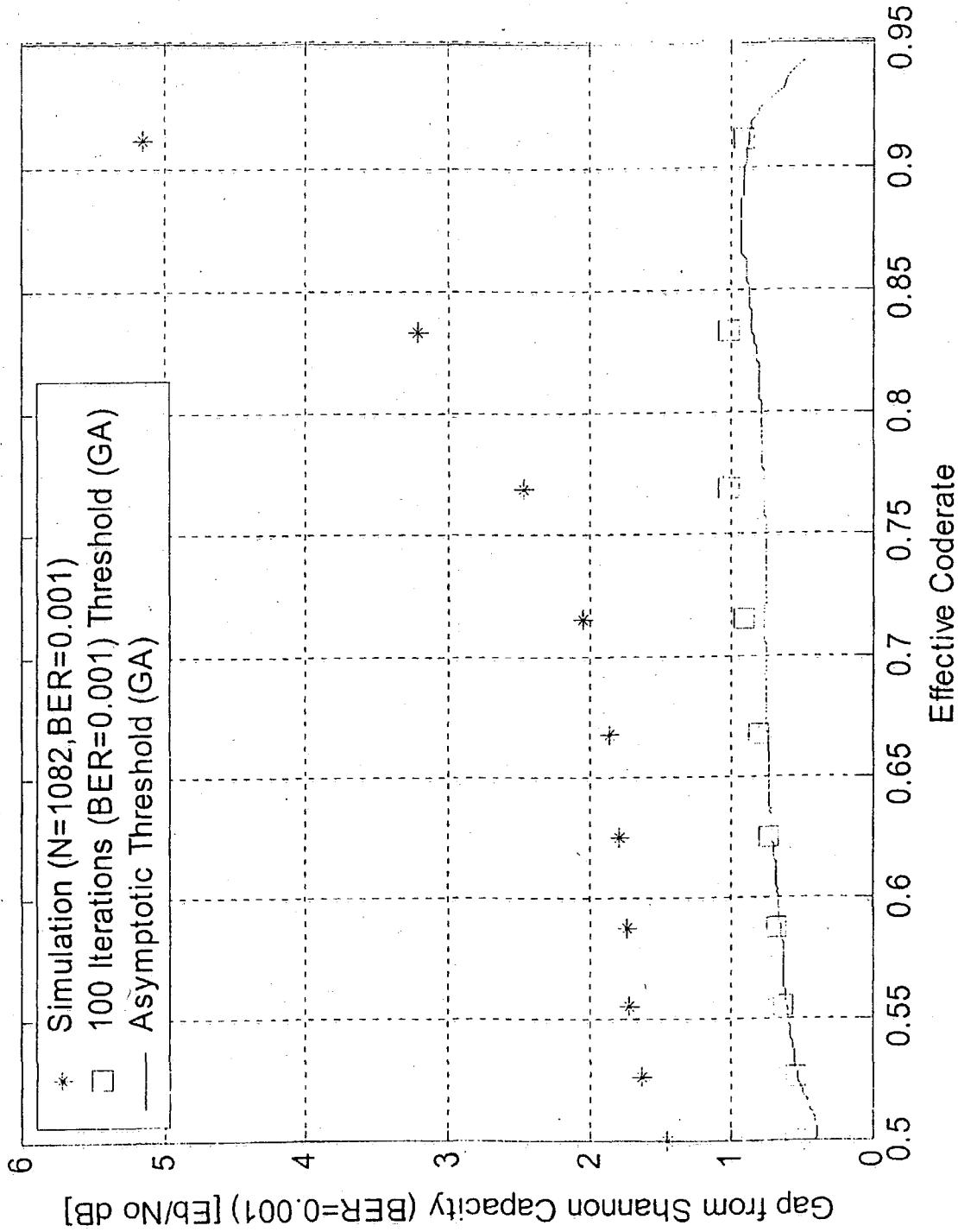


Figure 12

sim output N1082 M541Code12 100iters.mat

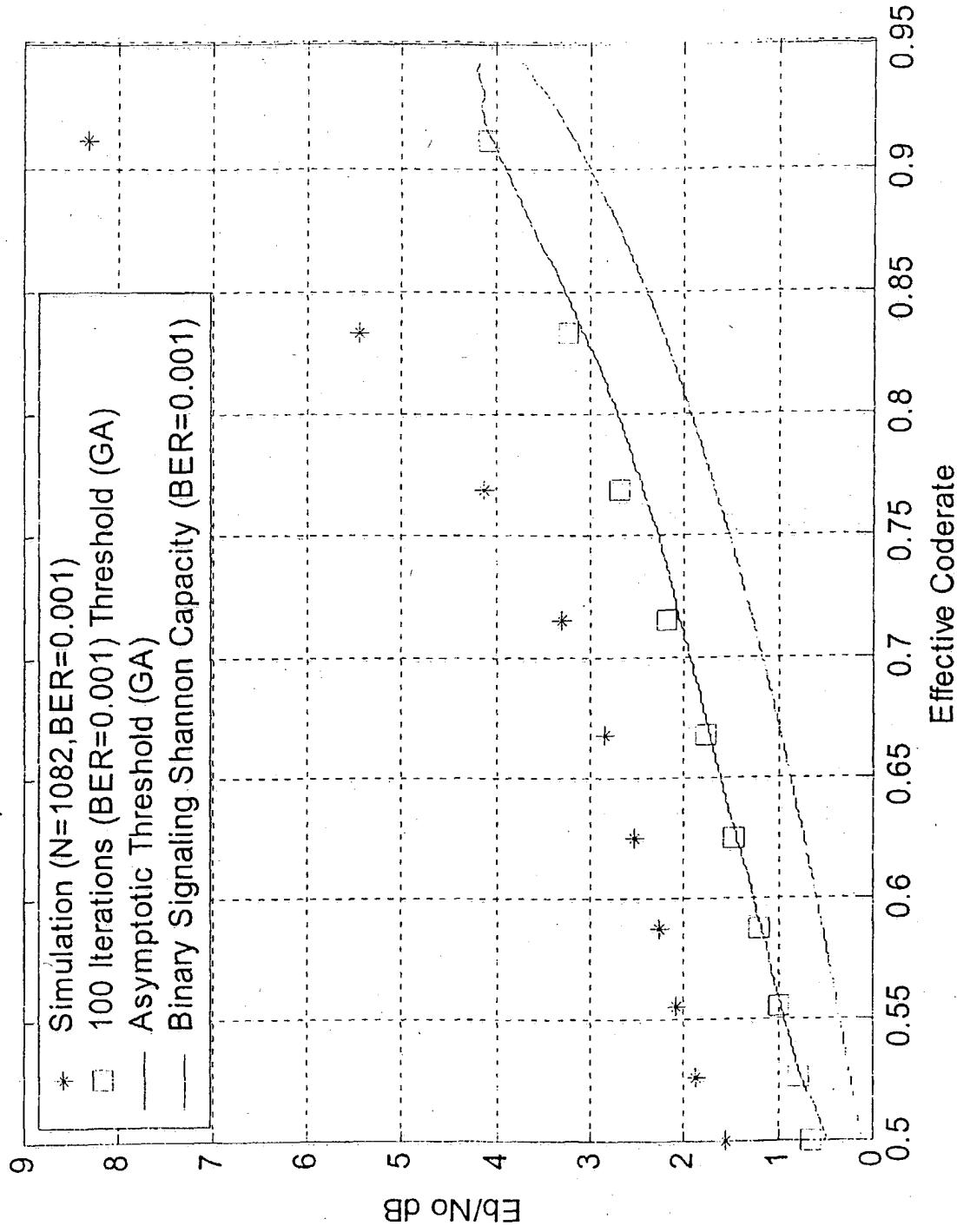


Figure 13

sim output N3255 M1627Code11 100iters.mat

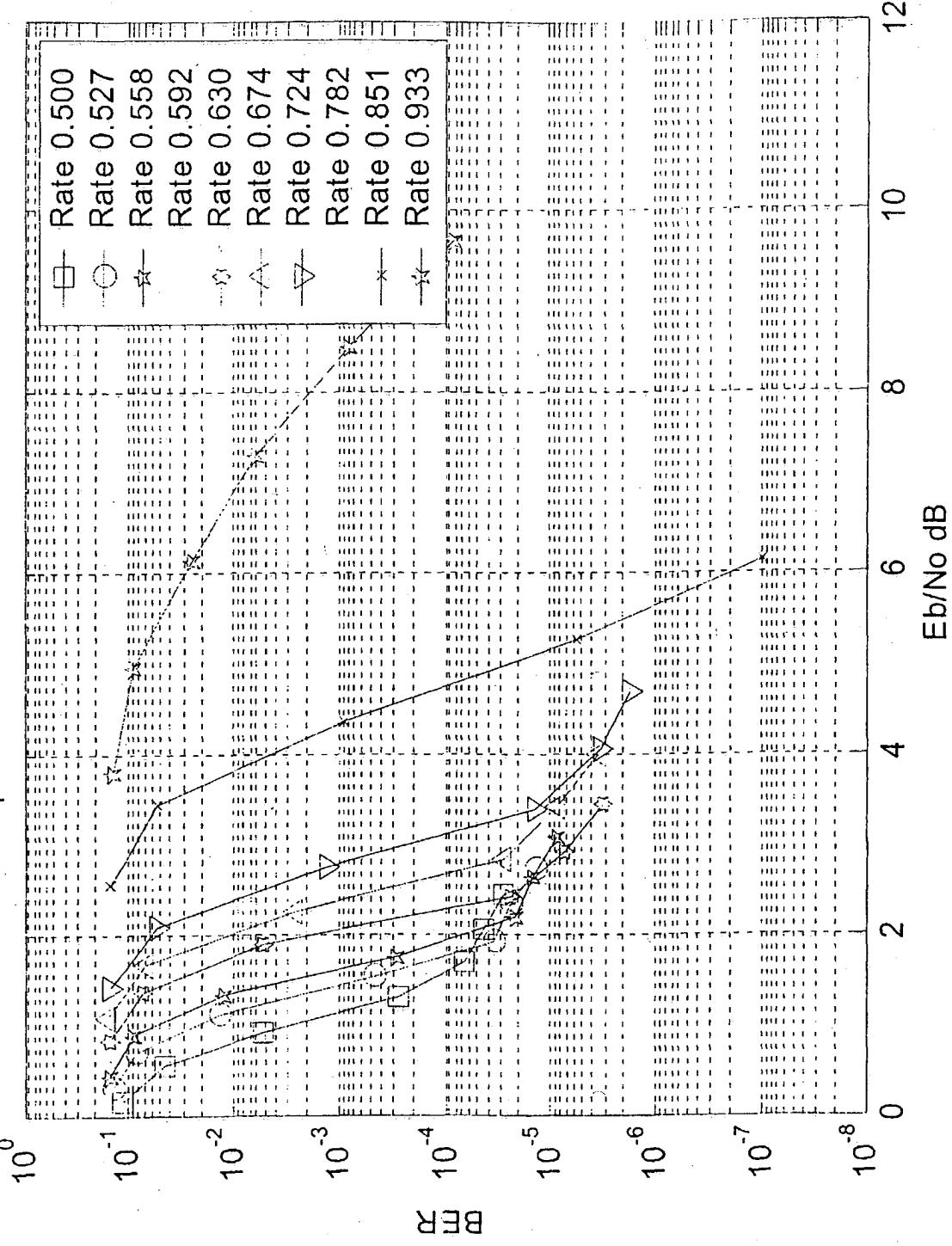


Figure 14

sim output N3255 M1627Code11 100iters.mat

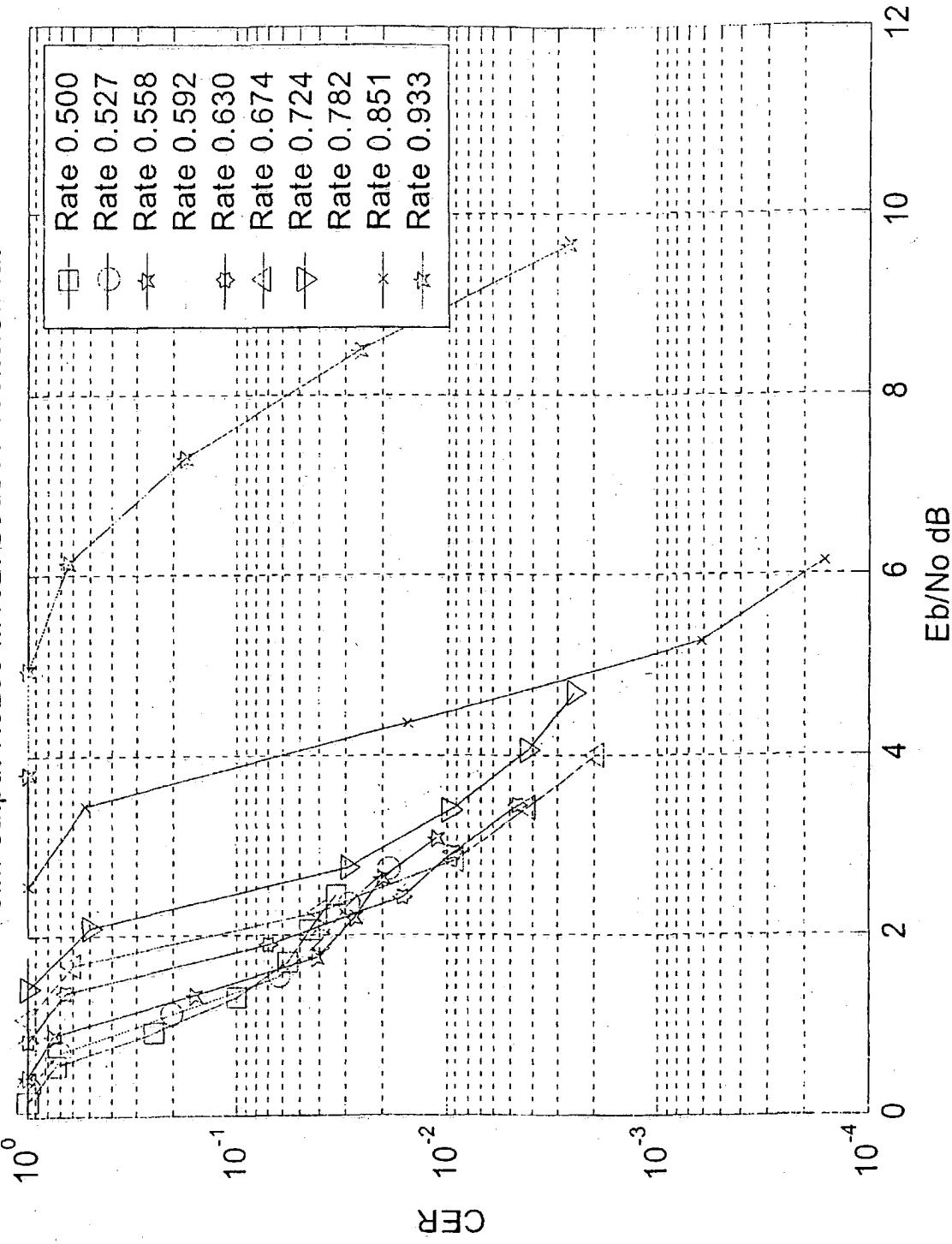


Figure 15

sim output N3255 M1627Code11 100iters.mat

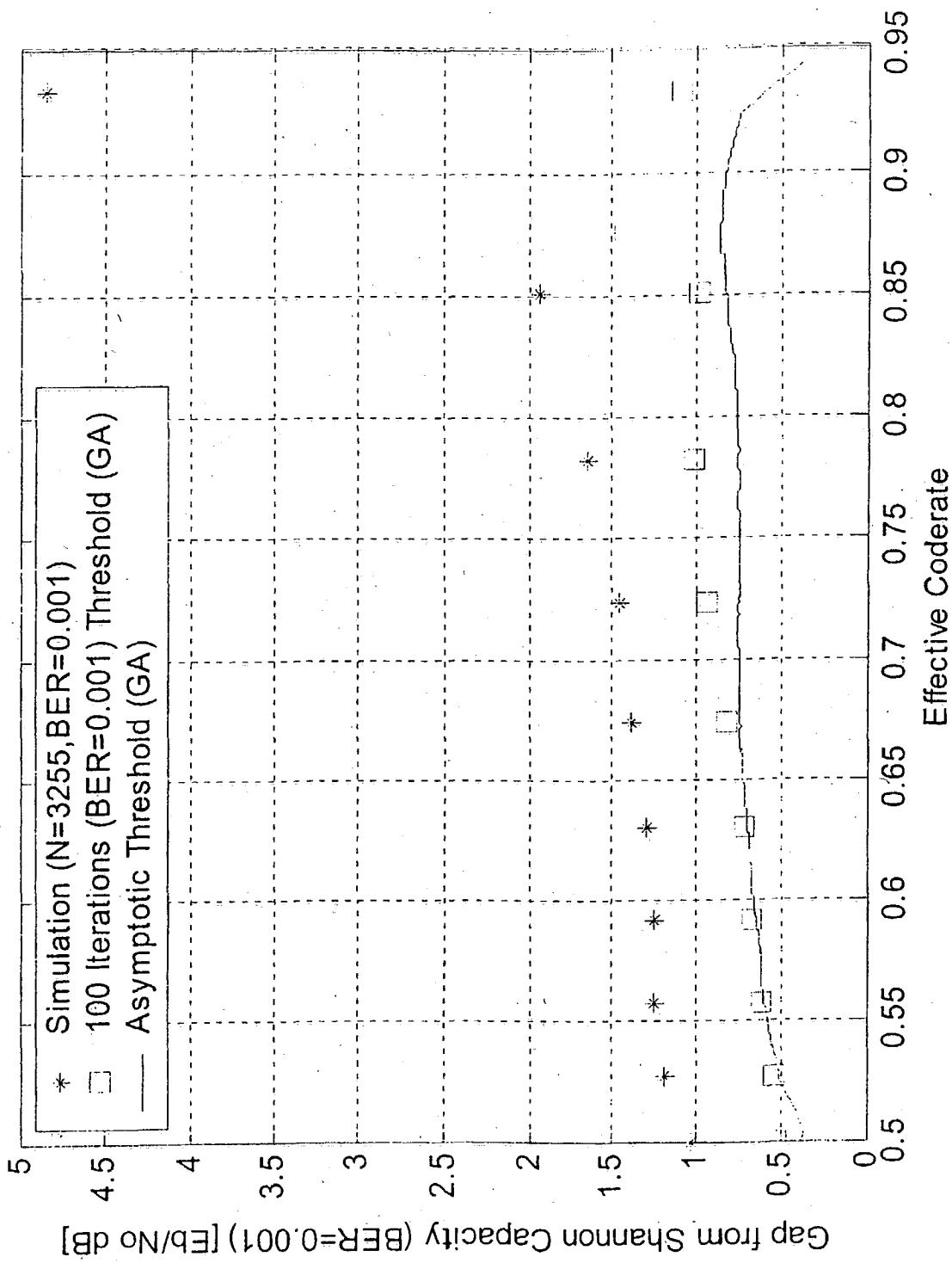


Figure 16

sim output N3255 M1627Code11 100iters.mat

